

40G QSFP+ to 40G QSFP+ Direct Attach Passive Copper Cables

Product Description

QSFP+ (Quad Small Form-factor Pluggable Plus) copper direct-attach cables are suitable for very short distances and offer a highly cost-effective way to establish a 40-Gigabit link between QSFP+ ports of QSFP+ switches within racks and across adjacent racks. These cables are used for 40GbE and Infniband standards, to maximize performance. QSFP+ are designed to meet emerging data center and high performance computing application needs for a high density cabling interconnect system capable of delivering an aggregate data bandwidth of 40Gb/s. This interconnect system is fully compliant with existing industry standard specifications such as the QSFP MSA and IBTA (InfiniBand Trade Association). The QSFP+ cables support the bandwidth transmission requirements as defined by IEEE 802.3ba (40 Gb/s) and Infiniband QDR (4x10 Gb/s per channel) specifications.



Features

- Compliant QSFP MSA specifications
- > Fully compatible with IEEE802.3ba and Infiniband QDR specifications
- > 40Gb/s total bandwidth
- 4 independent duplex channels operating at 10Gbps, also support for 2.5Gbps, 5Gbps data rates
- > 100 ohm differential impedance system
- ➤ Single 3.3V power supply, low power consumption, <1.5W
- Low Near-End Crosstalk(NEXT)
- Operating case temperature: -40 to 85°C
- All-metal housing for superior EMI performance
- > Precision process control for minimization of pair-to-pair skew
- AC coupling of PECL signals
- > EEPROM for cable signature & system communications
- > 30 AWG to 24 AWG cable sizes available
- RoHS compliant



Application

Data

- Servers
- Networked storage systems
- Routers
- External storage systems
- Data Center networking

Communications

- > Switches
- Routers

Industry Standards

- InfiniBand Trade Association (IBTA)
- ➤ IEEE802.3ba
- 40Gigabit Ethernet (40G BASE CR4)

Absolute Maximum Ratings

| Parameter | Symbol | Min | Max | Unit | Note |
|--------------------------------------|---------|------|-----------|------|------|
| Storage Temperature | Tst | -40 | 125 | degC | |
| Relative Humidity (non-condensation) | RS | - | 85 | | |
| Operating Case Temperature | Торс | -40 | 85 | degC | 1 |
| Supply Voltage | VCC3 | -0.3 | 3.6 | V | |
| Voltage on LVTTL Input | Vilvttl | -0.3 | VCC3 +0.2 | V | |

Note:

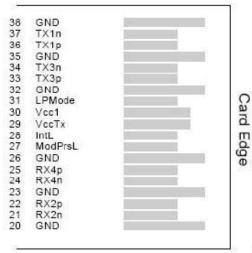
Stress or conditions exceed the above range may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those listed in the operational sections of this specification is not applied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

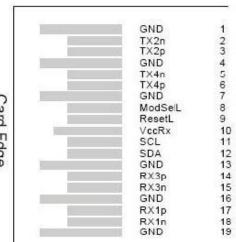
Recommended Operating Conditions

| Parameter | Symbol | Min | Max | Unit |
|--------------------------------------|--------|-------|-------|------|
| Operating Case Temperature | Торс | -40 | 85 | degC |
| Relative Humidity (non-condensation) | RS | - | 85 | % |
| Supply Voltage | VCC3 | 3.135 | 3.465 | V |
| Power Supply Currenct | ICC3 | 750 | - | mA |
| Total Power Consumption | Pd | - | 2.0 | W |



Pin Assignments and Descriptions





Top Side Viewed from Top

Bottom Side Viewed from Bottom

| PIN | Logic | Symbol | Name/Description | Note |
|-----|------------|---------|--------------------------------------|------|
| 1 | | GND | Ground | 1 |
| 2 | CML-I | Tx2n | Transmitter Inverted Data Input | |
| 3 | CML-I | Tx2p | Transmitter Non-Inverted Data output | |
| 4 | | GND | Ground | 1 |
| 5 | CML-I | Tx4n | Transmitter Inverted Data Input | |
| 6 | CML-I | Тх4р | Transmitter Non-Inverted Data output | |
| 7 | | GND | Ground | 1 |
| 8 | LVTLL-I | ModSelL | Module Select | |
| 9 | LVTLL-I | ResetL | Module Reset | |
| 10 | | Vcc Rx | +3.3V Power Supply Receiver | 2 |
| 11 | LVCMOS-I/O | SCL | 2-Wire Serial Interface Clock | |
| 12 | LVCMOS-I/O | SDA | 2-Wire Serial Interface Data | |
| 13 | | GND | Ground | |
| 14 | CML-O | Rx3p | Receiver Non-Inverted Data Output | |
| 15 | CMLO | Rx3n | Receiver Inverted Data Output | |
| 16 | | GND | Ground | 1 |
| 17 | CMLO | Rx1p | Receiver Non-Inverted Data Output | |
| 18 | CMLO | Rx1n | Receiver Inverted Data Output | |
| 19 | | GND | Ground | |
| 20 | | GND | Ground | 1 |
| 21 | CMLO | Rx2n | Receiver Inverted Data Output | |



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| 22 | CMLO | Rx2p | Receiver Non-Inverted Data Output | |
|----|--------|---------|-------------------------------------|---|
| 23 | | GND | Ground | 1 |
| 24 | CMLO | Rx4n | Receiver Inverted Data Output | 1 |
| 25 | CMLO | Rx4p | Receiver Non-Inverted Data Output | |
| 26 | | GND | Ground | 1 |
| 27 | LVTTLO | ModPrsL | Module Present | |
| 28 | LVTTLO | IntL | Interrupt | |
| 29 | | Vcc Tx | +3.3 V Power Supply transmitter | 2 |
| 30 | | Vcc1 | +3.3 V Power Supply | 2 |
| 31 | LVTTLI | LPMode | Low Power Mode | |
| 32 | | GND | Ground | 1 |
| 33 | CMLI | Тх3р | Transmitter Non-Inverted Data Input | |
| 34 | CMLI | Tx3n | Transmitter Inverted Data Output | |
| 35 | | GND | Ground | 1 |
| 36 | CMLI | Tx1p | Transmitter Non-Inverted Data Input | |
| 37 | CMLI | Tx1n | Transmitter Inverted Data Output | |
| 38 | | GND | Ground | 1 |
| | | | | |

Note:

- 1) GND is the symbol for signal and supply (power) common for QSFP modules. All are common within the QSFP module and all module voltages are referenced to this potential otherwise noted. Connect these directly to the host board signal common ground plane.
- 2) Vcc Rx, Vcc1 and Vcc Tx are the receiver and transmitter power suppliers and shall be applied concurrently. Recommended host board power supply filtering is shown below. Vcc Rx, Vcc1 and Vcc Tx may be internally connected within the QSFP transceiver module in any combination. The connector pins are each rated for a maximum current of 500mA.

Recommended power supply filtering Example of QSFP Host board schematics.

A typical host board mechanical layout for attaching the QSFP transceiver is presented below.

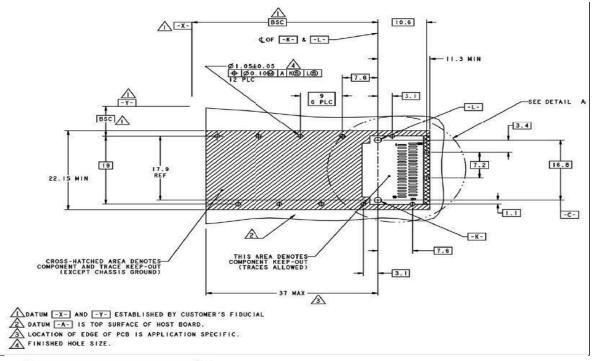
The recommended host electrical connector should be a 38-pin IPASS right angle connector assembly (example: Tyco PN: 1761987-9) and the cage assembly should be QSFP single cage (example: Tyco PN: 1888617-1).

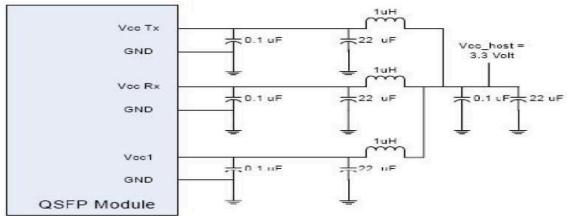
Recommended PCB layout

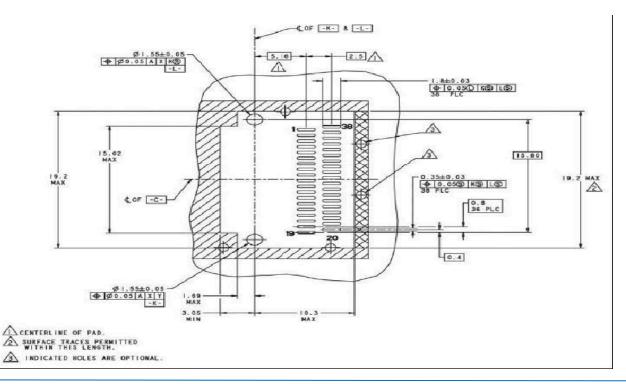
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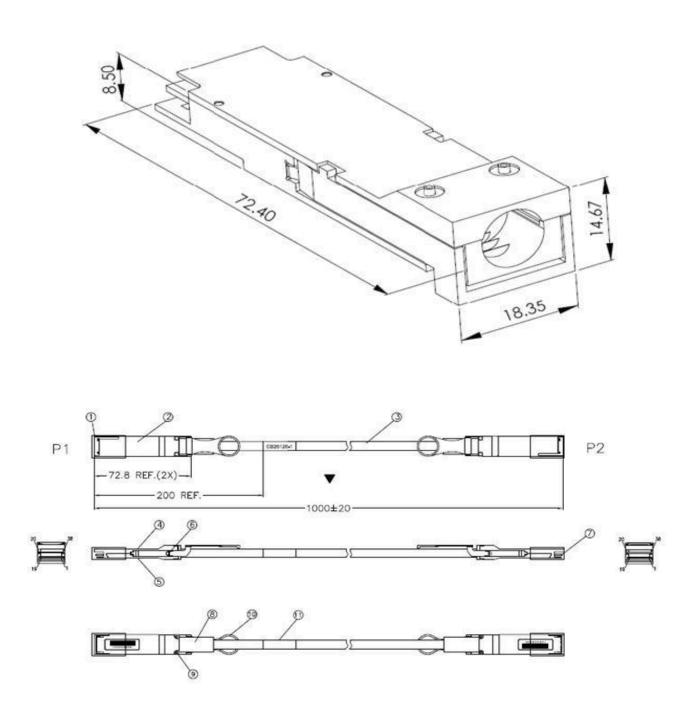








Mechanical Dimensions



Important Notice

Performance figures, data and any illustrative material provided in this data sheet are typical and must be specifically confirmed in writing by company before they become applicable to any particular order or contract. In accordance with company policy of continuous improvement specifications may change without notice. The publication of information in this data sheet does not imply freedom from patent or other protective rights of company or others. Further details are available from any company sales representative.



Ordering information

| Part Number | TK-QSPPDCA-324CXX | | | | | | |
|------------------|-------------------|----|----|-------|----|----|----|
| Length (meter) | 0.5 | 1 | 2 | 3 | 4 | 5 | 7 |
| Wire gauge (AWG) | 30 | 30 | 30 | 28/30 | 28 | 24 | 24 |

Regulatory Compliance

| Feature | Reference | Performance |
|------------------------------------|--|---------------------------|
| Electrostatic discharge (ESD) | IEC/EN 61000-4-2 | Compatible with standards |
| Electromagnetic Interference (EMI) | FCC Part 15 Class B EN 55022 Class B (CISPR 22A) | Compatible with standards |
| Laser Eye Safety | FDA 21CFR 1040.10, 1040.11 IEC/EN 60825-1, 2 | Class 1 laser product |
| Component Recognition | IEC/EN 60950, UL | Compatible with standards |
| ROHS | 2002/95/EC | Compatible with standards |
| EMC | EN61000-3 | Compatible with standards |